

METHOD AND SYSTEM FOR THE ADJUSTMENT OF AN INTERNAL
TIMING SIGNAL OR A CORRESPONDING REFERENCE IN AN
INTEGRATED CIRCUIT, AND CORRESPONDING INTEGRATED
CIRCUIT

Abstract of the Disclosure

A method for adjusting a duration of an
internal timing signal in an integrated circuit with a
value close to a typical value of the duration may
5 include activating the internal timing signal in the
integrated circuit and sequentially sending calibration
values to an input of the integrated circuit. The
expiration of the internal timing signal may determine
the last calibration value received or being received,
10 and the calibration data may be applied to a device for
adjusting the duration of the internal timing signal.

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